

SPBTLE-1S ArTM

October 2017

STMicroelectronics







BlueNRG Product Family

for smart innovators



Beacon / Retail



PC accessories



Medical



Toys / Gaming





















ePayment



Smart Home



Tags and Finders



Industrial



SPBTLE-1S Agenda



ST Bluetooth Low Energy modules ready to sell

Reminder: module approach rational

SPBTLE-1S BLE SOC module

Applications

FAQ



Bluetooth® SMART offering Roadmap

Discrete 4

Application Processor = SoC Network Processor

BlueNRG-MS

Cortex-M0 BLE 4.1 Master & Slave Output power: +8dBm Rx: 7.3mA Tx: 8.2mA@0dBm

QFN32, WCSP34

BlueNRG-1

BLE 4.2 Cortex-M0 160KB Flash, 24kB RAM I²C, SPI, UART, ADC Output power: +8dBm Rx: 7.3mA Tx: 8.2mA@ 0dBm

QFN32 (AEC), WCSP34

BLE 4.2: secure connection & privacy 1.2

BlueNRG-2

Cortex-M0 256KB Flash, 24kB RAM I2C, SPI, UART, ADC Output power: +8dBm Rx: 7.3mA

BLE 4.2

Tx: 8.2mA@ 0dBm QFN32 (AEC), **QFN 48**, WCSP34

BLE 4.2 : data packet extension lenght (Q4)

BlueNRG

BLE 4.0 Cortex-M0 Output power: +8dBm Rx:7.3mA Tx: 8.2mA@0dBm QFN32, WCSP34

no more use for new design

In production

MP





Bluetooth® SMART offering Roadmap

Modules 5



BLE 4.1

Master & Slave Output power: +4dBm

11.5x13.5x2

SPBTLE-RF0

FCC, CE BLE 4.1 Cortex-M0 Master & Slave Output power: +4dBm

11.5x13.5x2





SPBTLE-1S

11.5 mm x 13.5 mm

FCC, CE **BLE 4.2** Cortex-M0 160KB Flash, 24kB RAM I2C, SPI, UART, ADC Output power: +4dBm 11.5x13.5x2

BLE 4.2 : secure connection & privacy 1.2



Cortex-M0 **Master & Slave**

Output power: +8dBm

Rx: 7.3mA

Tx: 8.2mA@0dBm QFN32, WCSP34



Cortex-M0 160KB Flash, 24kB RAM I²C, SPI, UART, ADC Output power: +8dBm Rx: 7.3mA Tx: 8.2mA@ 0dBm QFN32 (AEC), WCSP34

In production



SPBTLE-1S Agenda

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SPBTLE-1S HW leverage



BlueNRG-1

Evaluation

HW design

SW implementation

Certif

MP



SPBTLE-1S

Evaluation

HW

SW implementation

MP

Modules designed for time to market





SPBTLE-1S Certified _______

Regional and BLE RF certifications

Modules designed for time to market













EU



US





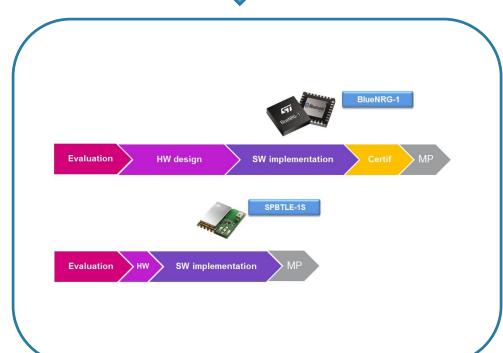




China

Japan (coming soon)









SPBTLE-1S



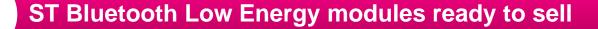


For which volume should I consider SPBTLE-1S vs. BlueNRG-1? Depends of course of BOM, application complexity, RF knowledge & certification required

	BOM \$	Dev \$	Certification\$
auding. 1		HW design more complex & RF knowledge required SW effort is identical	Regional RF certification = region × 8000\$ BT RF certification = 15000\$
		Easy HW design SW effort is identical	No certification needed



SPBTLE-1S Agenda 10



Reminder: module approach rational



SPBTLE-1S BLE SOC module

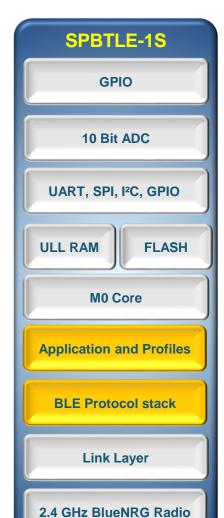
Applications

FAQ



SPBTLE-1S

Bluetooth 4.2 Module



SPBTLE-1S Module

Bluetooth v4.2 security compliant

Core & Memories

- Cortex-M0 @ 32MHz
- RAM: 2x12KB Ultra Low Leakage
- Flash: 160KB (60KB Minimum stack)

Peripherals

- Up to 14xGPIO
- 1xSPI, 2xI²C, 1xUART, 1x10-bit ADC
- Timer, ADC, WDG & RTC, PDM.

Extended Operating range

- 1.7 up to 3.6V
- 40 up to +85°C

Radio Performance

- Up to +5dBm
- Rx 7.3 mA and Tx 8.2 mA @ 0dBm
- Sleep current < 1µA

Packages

- 11.5x13x2
- Compatible with SPBTLE-RF















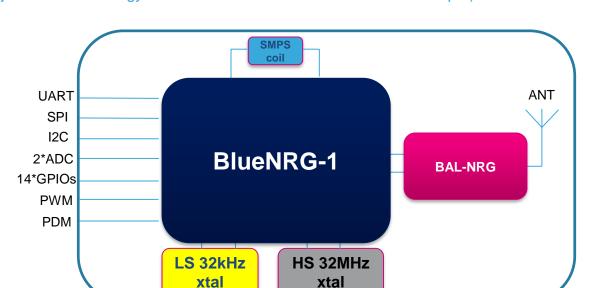




SPBTLE-1S HW integration

- SPBTLE-1S is a 11.5*13.5*2 mm module integrating:
 - BlueNRG-1 SOC (CSP package)
 - BAL-NRG-01D3 balun
 - 32kHz & 32MHz Xtal
 - 10uH SMPS coil
 - 2.4Ghz ceramic antenna

(http://www.johansontechnology.com/datasheets/antennas/2450AT18A100.pdf)



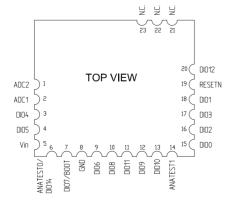






SPBTLE-1S Pinning and IOs

SPBTLE-1S pinning



SPBTLE-1S IOs

- Below table summarizing IOs and possible functions
- SPBTLE-1S is base don BlueNRG-1 CSP so DIO13 not available
- If using SPI interface, pay attention to wake up capabilities of IO selected

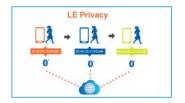
	Special feature	GPIO mode "000"			Mode serial 1 "001"		Mode serial 0 "100"		Microphone / ADC mode "101"		
		Type	Reset	Pup/down	Signal	Type	Signal	Type	Signal	Type	Customer
DIO0		1/0	I PD	PD	GPIO 0	_	UART_CTS	1/0	SPI_CLK		
DIO1		1/0	I PD	PD	GPIO 1	0	UART_RTS	1/0	SPI-CS1	1	PDM_DATA
DIO2		1/0	I PD	PD	GPIO 2	0	PWM0	0	SPI_OUT	0	PDM_CLK
DIO3		1/0	I PD	PD	GPIO 3	0	PWM1	_	SPI-IN	0	ADC_CLK
DIO4		1/0	I PD	PD	GPIO 4	- 1	UART_RXD	1/0	I2C2_CLK	0	PWM0
DIO5		1/0	I PD	PD	GPIO 5	0	UART_TXD	1/0	I2C2_DAT	0	PWM1
DIO6		1/0	I PD	PD	GPIO 6	0	UART_RTS	1/0	I2C2_CLK	_	PDM_DATA
DIO7	Boot mode (IO7 high)	1/0	I PD	PD	GPIO 7	_	UART_CTS	1/0	I2C2_DAT	0	PDM_CLK
DIO8		1/0	I PD	PD	GPIO 8	0	UART_TXD (bootloader)	1/0	SPI_CLK	1	PDM_DATA
DIO9	wake up capable	1/0	I PU	PU	GPIO 9	1	SWCLK	_	SPI-IN		
DIO10	wake up capable	1/0	I PU	PU	GPIO 10	- 1	SWDIO	0	SPI_OUT		
DIO11	wake up capable	1/0	I PU	PU	GPIO 11	- 1	UART_RXD (bootloader)	1/0	SPI-CS1		
DIO12	wake up capable	OD	1	No Pull	GPIO 12	1		1/0	I2C1_CLK		·
DIO14		1/0	I PD	PD	GPIO 14	1/0	I2C1_CLK	1/0	SPI_CLK	0	ADC_DATA





SPBTLE-1S

BLE 4.2 secure features







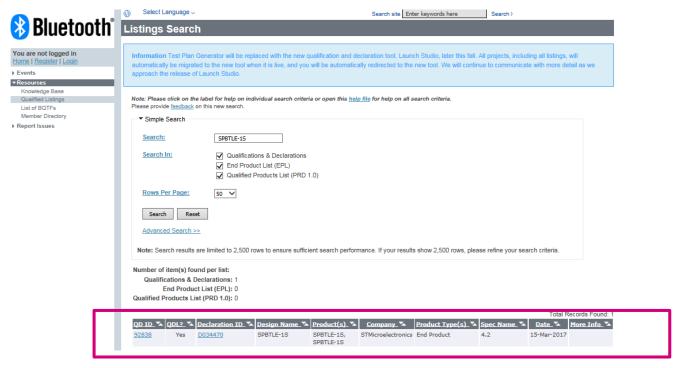
	BlueNRG-1 SPBTLE-1S	BlueNRG-2
privacy 1.2	@st. com DK 2.5.0	@st. com DK 2.5.0
secure connection 4.2	@st. com DK 2.5.0	@st. com DK 2.5.0
data packet extended length		end Q4





SPBTLE-1S BLE SW stack capability

BLE 4.2 End Product certified





QDID 92838 | SPBTLE-1S | End Product | BLE 4.2



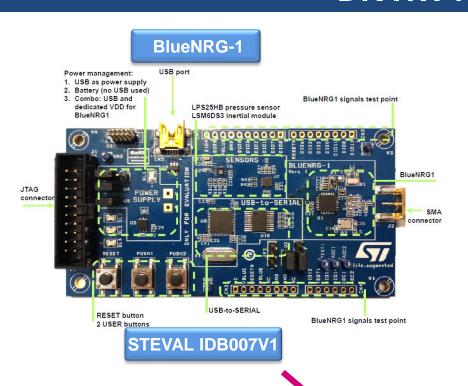




SPBTLE-1S Development Tools

HW material ^{■ 16}

DK HW Resources



SPBTLE-1S



STEVAL IDB007V1M

- 1 SW development kit
- @ STSW-BLUENRG1-DK





SPBTLE-1S Development Tools SW material – BlueNRG-1 reuse (1/2) I

SW DK - BLE examples









 BlueNRG-1 STEVAL-IDB007V1 is based on 16Mhz, default images and associated code examples are so based on 16Mhz





 Compilation option need to be modified in IDE build options to be compliant to SPBTLE-1S integrating a 32Mhz



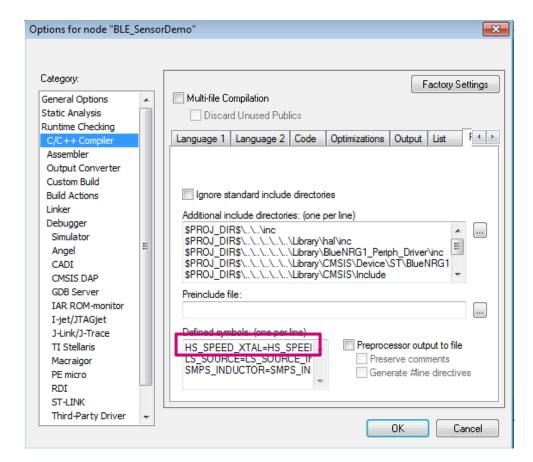


HS_SPEED_XTAL=HS_SPEED_XTAL_32MHZ



SPBTLE-1S Development Tools SW material – BlueNRG-1 reuse (1/2) 18

SW DK - BLE examples





HS SPEED XTAL=HS SPEED XTAL 16MHZ



HS SPEED XTAL=HS SPEED XTAL 32MHZ



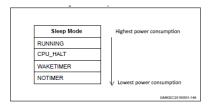


SPBTLE-1S Development Tools SW material – BlueNRG-1 reuse (2/2)

SW DK - Documentation



PM0257 programming guide: BLE concepts & associated ST API Doxygen documentation: BlueNRG-1 BLE and peripherals API



<u>AN4820</u>: BlueNRG-1 Low Power modes <u>AN4869</u>: BlueNRG-1 Firmware Upgrade Over the Air



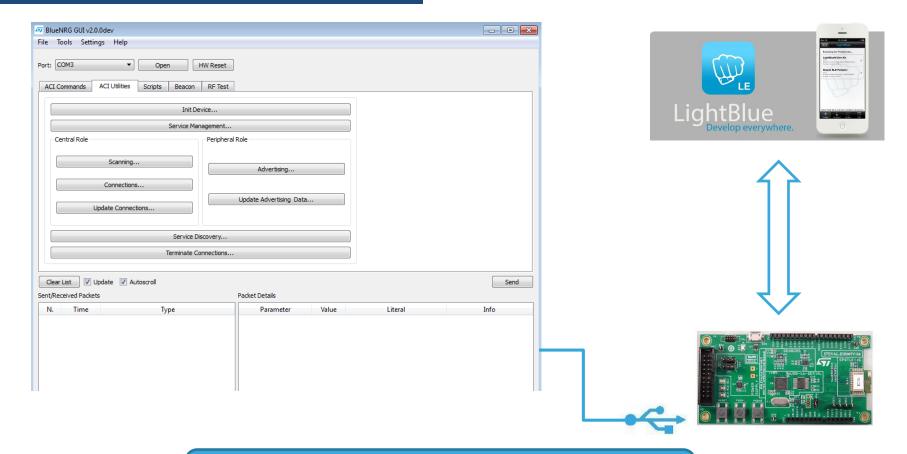


SPBTLE-1S Development Tools

Tool material 20

@ STSW-BNRGUI

ST BLE GUI





Comprehensive GUI to understand BLE concept and associated ST APIs



SPBTLE-1S Development Tools Navigator promotion tool not compatible









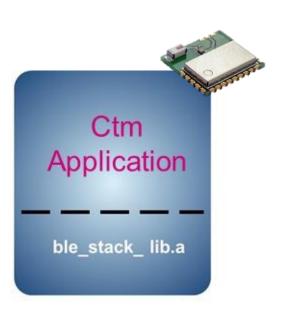


SPBTLE-1S / BlueNRG-1/BlueNRG-2

Architecture options (1/2) 22

SPBTLE-1S as unique SOC





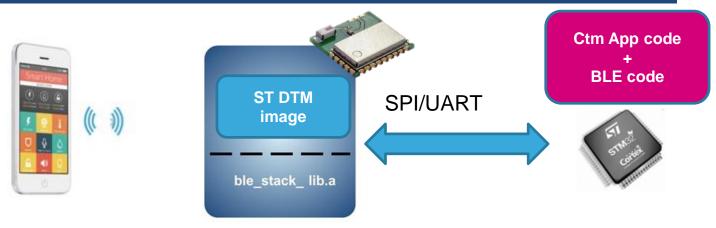




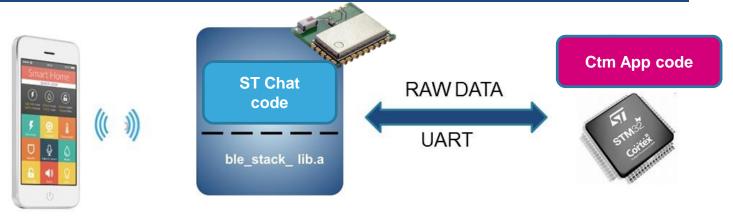
SPBTLE-1S / BlueNRG-1/BlueNRG-2

Architecture options (2/2) 23

SPBTLE-1S as true network processor



SPBTLE-1S as data pump: UART serial port emulation over BLE





SPBTLE-1S Modules on the web

support tools

Module



Order code	Description
SPBTLE-1S (Tray)	Bluetooth Low Energy Application Processor Module – 4.2
SPBTLE-1STR (Tape & Reel)	Bluetooth Low Energy Application Processor Module – 4.2

Evaluation boards



Order code	Description
STEVAL-IDB007V1M	Evaluation platform based on SPBTLE-1S Module
STEVAL-BLUEMIC1	Ultra low power Bluetooth low energy microphone based on SPBTLE-1S

Other tools



Order code	Description		
STSW-BLUENRG1-DK	Software Design Kit		
STSW-BNRGUI	Graphical User Interface		
STSW-BNRG1STLINK	ST-LINK utility		
STSW-BNRG001	Power consumption estimation tool		
STSW-BLEPROFILES	Bluetooth low energy profiles		



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Reminder: module approach rational

SPBTLE-1S BLE SOC module



Applications

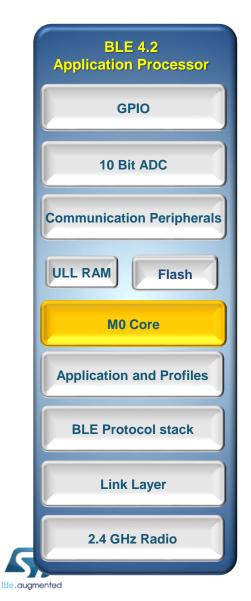
FAQ





BlueNRG-1, SPBTLE-1S, BlueNRG-2

Applications 26



enable low to mid end smart connected applications





Beacon Sensor tags Remote Control



Remote Controller – Turn Key solution





STEVAL-BLUEMIC1

Based on SPBTLE-1S: BlueNRG-1 Module

- Single chip RCU turn-key solution
 - Control + Voice wireless connectivity
 - Leverage on BlueVoice for Voice-over-BLE
 - Low cost PCB with QFN package
 - Software upgrade via BLE





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Reminder: module approach rational

SPBTLE-1S BLE SOC module

Applications



FAQ







SPBLTE-1S FAQ (1)

- What is maximum output power?
 - The output power is limited to +5dbm
 - Nothing prevent user to set higher power but certification is then no more valid
- Can I access to all BlueNRG-1 I/Os?
 - SPBTLE-1S IOs are exactly the one from BlueNRG-1 in CSP package
 - It means DIO13 is not available with SPBTLE-1S
- Can I host application inside SPBTLE-1S?
 - Of course, and hope documentation was clear enough
 - Specific documentation available from rf-support-emea@st.com
- Can I order STEVAL-IDB007V1M?
 - Eval kit is available and orderable

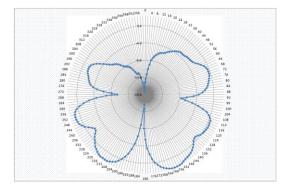








- Where can I found certification evidence?
 - Full certification package available from rf-support-emea@st.com
- What is antenna used and what are module radiated performances?
 - Antenna used is a ceramic antenna from Johanson
 http://www.johansontechnology.com/datasheets/antennas/2450AT18A100.pdf
 - Radiation pattern is also available





ANY QUESTIONS? rf-support-emea.com

