

NOTE:  
 $V_{IH} > 0.7 \times V_{DD}$   
 $V_{IL} < 0.6V$

INPUT SUPPLY VOLTAGE:  
 3.1 TO 3.6 V

INPUT SUPPLY VOLTAGE: 3.0 TO 3.6 V  
 $I_{ddMAX} = 23mA$

LETTER IN PART NAME:  
 X= A FOR CHIP ANTENNA  
 X= C FOR U.FL CONNECTOR

NOTE:  
 $V_{IH} > 0.7 \times V_{DD} = 2.31V$   
 $V_{IL} < 0.3 \times V_{DD} = 0.99V$   
 ADDR: 1001 000 b  
 EVENT DISABLED IN CONFIG REG

03.18/2013. NOTE FROM ST:  
 IN A STM32 POWERED IN A VOLTAGE RANGE OF  
 2-2.7V THE  $V_{OH\_min}$  IS  $V_{DD}-0.4V$  (@ $I_{out} = 6mA$ )  
 I.E. FOR GPIO POWERED AT 2V5  $V_{OH\_min} = 2.1V$

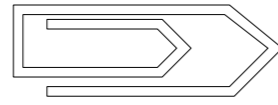
CPU ON WiFi MODULE IS STM32F103  
 POWER SUPPLY  $V_{DD} = 2V5$

GPIO  $V_{OHmin}$  @6mA=  $V_{DD}-0.4V = 2.1V$

5V TOL PINS  
 $V_{inMAX} = 5.5V$  (@ $V_{DD} > 2V$ )  
 OTHER PINS


$V_{inMAX} = V_{DD} + 0.3V$  (4V if  $I_{injMAX} < +/-5mA$ )

RSR CODE	956	REV.	B	DATE	24/06/2013	SHEET	1/2
COMPANY NAME	Architech		TITLE		WiFi EVAL		
by RSR srl		BOARD NAME		Branca			



## HISTORY OF UPDATES HARDWARE RELEASES

REVISION	DATE	DESCRIPTION / CHANGES
-	18/03/2013	INITIAL REVISION : PRELIMINARY DOCUMENTATION
B	25/06/2013	FIRST PRODUCTION RELEASE
B	==/==/2013	LABORATORY TESTS AND CERTIFICATION

RSR CODE 956	REV. B	DATE 24/06/2013	SHEET 2/2
COMPANY NAME	TITLE Z-HISTORY		BOARD NAME
 ArchiTech <small>SILICA Design Tools</small>	by RSR srl	Branca	